K.S. GROUP OF INSTITUTIONS, BENGALURU - 560 109

K.S. SCHOOL OF ENGINEERING AND MANAGEMENT. BENGALURU-560109

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

SESSION: YEAR : 2021-2022 (EVEN SEMESTER)

CLASS TIME TABLE

(w.e.f. 25th April 2022)

CLASS : VI ECE 'A' Class Teacher: Mrs.Jayashree G R Room No: A210

DAY	8.40-9:35	9:35-10:30	10:30-10:45	10:45-11:40	11:40-12:35	12:35-1:20	1:20-2:10	2:10-3:00	3:00-3:50
MONDAY	ES LAB A1 BATCH (DJ, BV) COMM.LAB A2 BATCH (MDK,JGR)				Open Ele.		M&A	DC	LIB
TUESDAY	M&A	ES	TEA BREAK	Prof. Ele.1	DC	LUNCH BREAK	ES LAB A2 BATCH (DJ, RBA) COMM.LAB A3 BATCH (MDK,JGR)		
WEDNESDAY	ES	M&A		Prof. Ele.1	DC		M&A	DC	ES
THURSDAY	DC	Prof. Ele.1		ES	Open Ele.		ES LAB A3 BATCH (SBN,BV) COMM. LAB A1 BATCH (MDK, HPD)		
FRIDAY	M&A	ES		Prof. Ele.1	Open Ele.		Mini Project		
SATURDAY	As Per COE			As Per COE			As Per COE		

CODE	SUBJECT	HOURS /WEEK	STAFF
18EC61	Digital Communications	5	Mrs. Jaishree G R
18EC62	Embedded Systems	5	Mr. Dileep J
18EC63	Microwave & Antennas	5	Dr. Karthik P
18EC641	Operating System Professional Elective-1	4	Dr. Karthik P
18EC644	Digital System Design using Verilog Professional Elective-1	4	Mrs. Swati Sarkar
18EC646	Python Application Programming Professional Elective-1	4	Mr. Sanjay B Nayak
18EC65X	Open Elective-A	3	Other Department Faculty
18ECL66	Embedded Systems Laboratory	3	Mr. Dileep J /Mrs. Bhargavi V / Mr. Ravikiran B A/ Mr. Sanjay B Nayak
18ECL67	Communication Laboratory	3	Mr. Manu D K/Mrs. Jaishree G R
18ECMP68	Mini-Project	1	Dr. Senthil Babu / Mrs. Bhargavi V

Time Table Co-ordinator

Professor & Head
Dept. of Electronics & Communication Engineering
K. S. School of Engineering & Management
Bangalore-560 109

HoD

Principal

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K.S. SCHOOL OF ENGINEERING AND MANAGEMENT, BENGALURU-560109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

SESSION: YEAR : 2021-2022 (EVEN SEMESTER)

CLASS TIME TABLE

(w.e.f. 25th April 2022)

CLASS : VI ECE 'B' Class Teacher: Mrs. Bhargavi V Room No: A209A

DAY	8.40-9:35	9:35-10:30	10:30-10:45	10:45-11:40	11:40-12:35	12:35-1:20	1:20-2:10	2:10-3:00	3:00-3:50
MONDAY	DC	ES	TEA BREAK	M&A	Open Ele.	LUNCH BREAK	ES LAB B1 BATCH (RBA,BV) COMM.LAB B2 BATCH (PS,HPD)		
TUESDAY	M&A	DC		Prof. Ele.1	ES		Mini Project		
WEDNESDAY	ES	M&A		Prof. Ele.1	ES		ES LAB B2 BATCH-(SBN,BV) COMM. LAB B3 BATCH (PS,HPD)		
THURSDAY	DC	Prof. Ele.1		M&A	Open Ele.		DC	M&A	LIB
FRIDAY	ES	DC		Prof. Ele.1	Open Ele.		ES LAB B3 BATCH (SBN,DJ) COMM. LAB B1 BATCH (PS,JGR)		
SATURDAY	As Per COE			As Per COE			As Per COE		

CODE	SUBJECT	HOURS /WEEK	STAFF
18EC61	Digital Communications	5	Mr. Puneeth S
18EC62	Embedded Systems	5	Mrs. Bhargavi V
18EC63	Microwave & Antennas	5	Mrs. Jaishree G R
18EC641	Operating System Professional Elective-1	4	Dr. Karthik P
18EC644	Digital System Design using Verilog Professional Elective-1	4	Mrs. Swati Sarkar
18EC646	Python Application Programming Professional Elective-1	4	Mr. Sanjay B Nayak
18EC65X	Open Elective-A	3	Other Department Faculty
18ECL66	Embedded Systems Laboratory	3	Mr.Dileep J/Mrs. Bhargavi V /Mr. Ravikiran B A/ Mr. Sanjay B Nayak
18ECL67	Communication Laboratory	3	Mr. Puneeth S /Mrs. Swati Sarkar
18ECMP68	Mini-Project	1	Dr. Senthil Babu / Mrs. Bhargavi V

Time Table Co-ordinator

Professor & Head
Dept. of Electronics & Communication Engineering
K. S. School of Engineering & Management
Bengaluru-560 109

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

SESSION: YEAR : 2021-2022 (EVEN SEMESTER)

CLASS TIME TABLE

(w.e.f. 25th April 2022)

CLASS : VI PARALLEL **Class Teacher: Mrs.Jayashree G R** **Room No: A209**

DAY	8.40-9:35	9:35-10:30	10:30-10:45	10:45-11:40	11:40-12:35	12:35-1:20	1:20-2:10	2:10-3:00	3:00-3:50
MONDAY	ARM LAB (NEWFACULTY, BV)				VLSI	LUNCH BREAK	CCN	DC	DSS
TUESDAY	DSS	ARM	TEA BREAK	CCN	DC		CN LAB (KP,SB)		
WEDNESDAY	ARM	DSDV		VLSI	DC		VLSI	DC	ARM
THURSDAY	DC	DSDV		ARM	DSS		CCN	LIB	
FRIDAY	DSDV	ARM		VLSI	CCN		DSDV	DSS	
SATURDAY	As Per COE			As Per COE			As Per COE		

CODE	SUBJECT	HOURS /WEEK	STAFF
17EC61	Digital Communications	5	Mrs. Jaishree G R
17EC62	ARM Microcontroller & Embedded Systems	5	Mr.Dileep J
17EC63	VLSI Design	4	Mrs. Shilpa A P
17EC64	Computer Communication Network	4	Dr. Senthil Babu K
17EC654	Digital Switching Systems Professional Elective-2	4	Mrs. Bhargavi V
17EC663	Digital System Design using Verilog	4	Mrs. Swati Sarkar
17ECL67	Embedded Controller Laboratory	3	Mr.Dileep J/Mrs. Bhargavi V
17ECL68	Computer Networks Laboratory	3	Dr. Senthil Babu K / Dr. Karthik P

Time Table Co-ordinator

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