



K.S. SCHOOL OF ENGINEERING AND MANAGEMENT, BENGALURU-560109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

SESSION: YEAR : 2024-2025 (ODD SEMESTER)

CLASS TIME TABLE

(w.e.f. 19th AUG-2024)

CLASS : III 'A' ECE

Class Teacher: Mrs. JAYASHREE G R

Room No: A310

| DAY | 8.40-9:35 | 9:35-10:30 | 10:30-10:45 | 10:45-11:40 | 11:40-12:35 | 12:35-1:20 | 1:20-2:10 | 2:10-3:00 | 3:00-3:50 |
|-----------|------------|------------|-------------|-------------|-------------|-------------|--|-----------------|-----------|
| MONDAY | CO | MATHS | TEA BREAK | NA | DSDV | LUNCH BREAK | A1 BATCH-ANALOG & DIGITAL SYSTEM LAB A2 BATCH- AEC-III LAB(LABVIEW) | | |
| TUESDAY | DSDV | NA | | EPC | SCR | | MATHS | SPORTS/NSS/YOGA | |
| WEDNESDAY | NA | EPC | | MATHS | CO | | A2 BATCH-ANALOG & DIGITAL SYSTEM LAB A3 BATCH- AEC-III LAB(LABVIEW) | | |
| THURSDAY | CO | DSDV | | IPCC LAB | | | NA | EPC | SCR |
| FRIDAY | EPC | DSDV | | MATHS | CO | | A3 BATCH-ANALOG & DIGITAL SYSTEM LAB A1 BATCH- AEC-III LAB(LABVIEW) | | |
| SATURDAY | As Per COE | | | | | As Per COE | | | |

| CODE | SUBJECT | HOURS /WEEK | STAFF |
|---------------------|--|-------------|-----------------------------------|
| BMATEC301 | AV MATHEMATICS-III FOR EC ENGINEERING | 4 | Dr. Vinutha S V |
| BEC302 | DIGITAL SYSTEM DESIGN USING VERILOG | 4 | Mr. Syed Waseem Tabraiz |
| BEC303 | ELECTRONIC PRINCIPLES & CIRCUITS | 4 | Mrs.Hemapriya M |
| BEC304 | NETWORK ANALYSIS | 4 | Mr. Gopalakrishnaurthy C R |
| BECL305 | ANALOG & DIGITAL SYSTEM DESIGN LAB | 9 | Mrs. Tejaswini G V/ Mr. Dileep J |
| BEC306C | COMPUTER ORGANIZATION & ARCHITECTURE | 4 | Mrs. Jayashree G R |
| BCK307 | SOCIAL CONNECT & RESPONSIBILITY | 2 | Mrs. Muskaan Huda R |
| BEC358A | AEC_III (LABVIEW PROGRAMMING) | 9 | Mrs. Jayashree G R/ Dr. Kishore M |
| BNSK359/ BPEK359 | National Service Scheme (NSS)/ Physical Education (PE) (Sports and Athletics) | 2 | Dr. Renuka V Tali/ Dr. Manu D K |

Manu D K
Time Table Co-ordinator

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