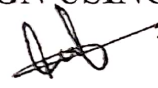

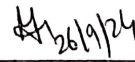



K. S. School of Engineering and Management
Department of Electronics and Communication Engineering
First Internal Test Time Table
Academic Year 2024-25(ODD Sem)


Semester- III

Date: 23/09/2024

Sl. No.	Date/Day	Morning Session Time: 9.30 am To 10.45 am	After Noon Session Time: 1.30 pm To 2.45 pm
		Subject (Code)	Subject (Code)
1	30/09/2024 Monday	AV MATHEMATICS-III (BMATEC301)	DIGITAL SYSTEM DESIGN USING VERILOG (BEC302)  (Timing:1:30-3:00 pm)
2	01/10/2024 Tuesday	ELECTRONIC PRINCIPLE & CIRCUITS (BEC303) (Timing:9:30-11:00 am)	COMPUTER ORGANIZATION & ARCHITECTURE (BEC306C) 
3	03/10/2024 Thursday	NETWORK ANALYSIS (BEC304) 	


Signature of the Test Coordinator


Signature of the HOD
Dept. of Electronics & Communication Engineering
K.S. School of Engineering & Management
Bangalore - 560 109


Signature of the Principal
Principal/Director
K S School of Engineering and Management
Bangalore - 560 109