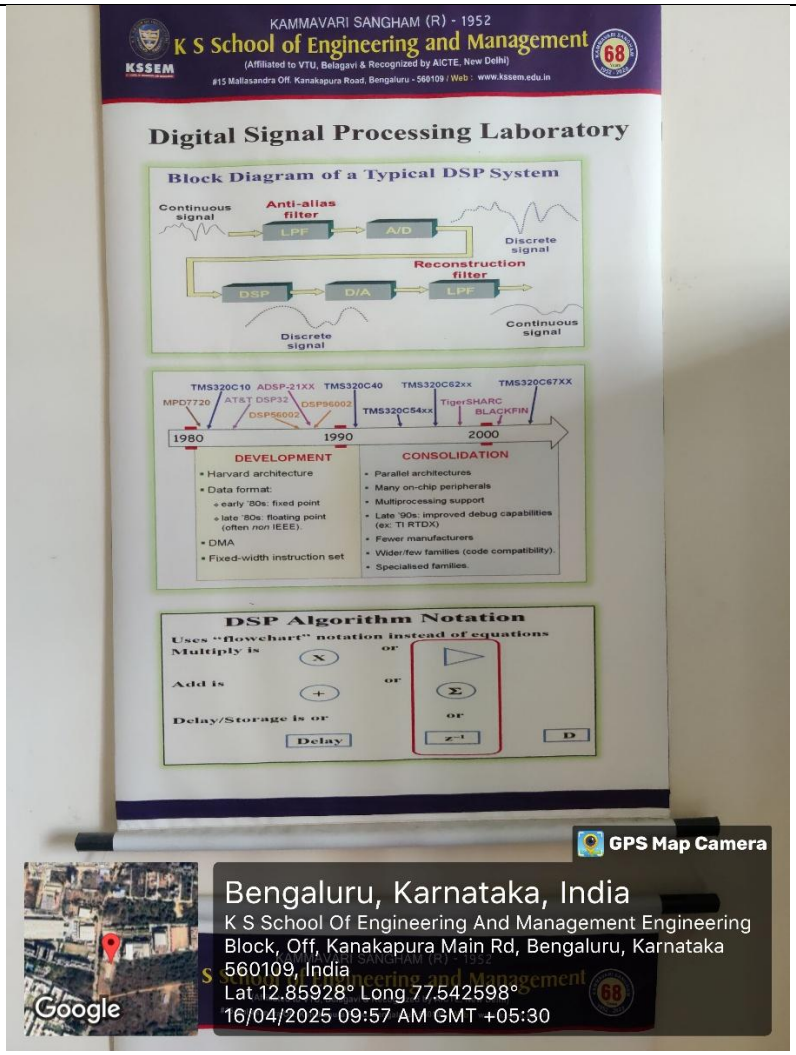
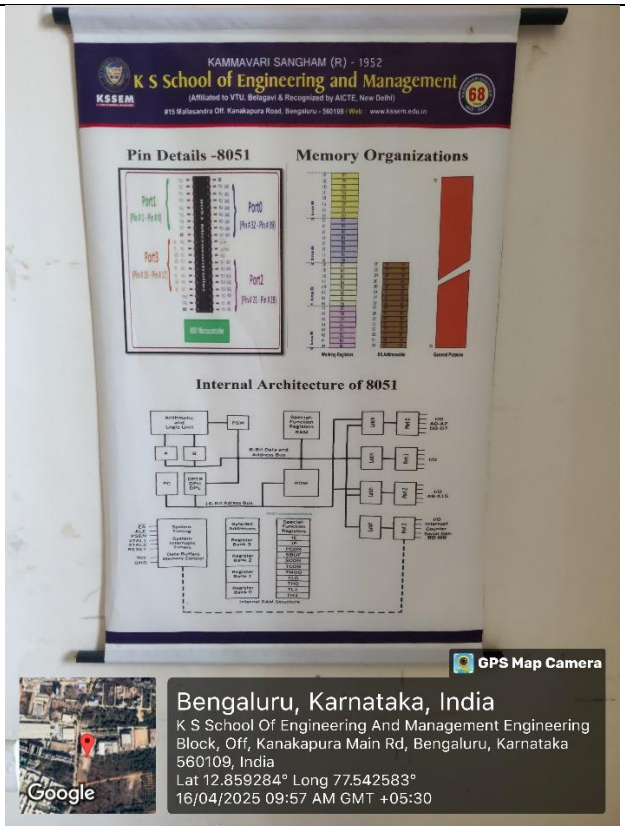
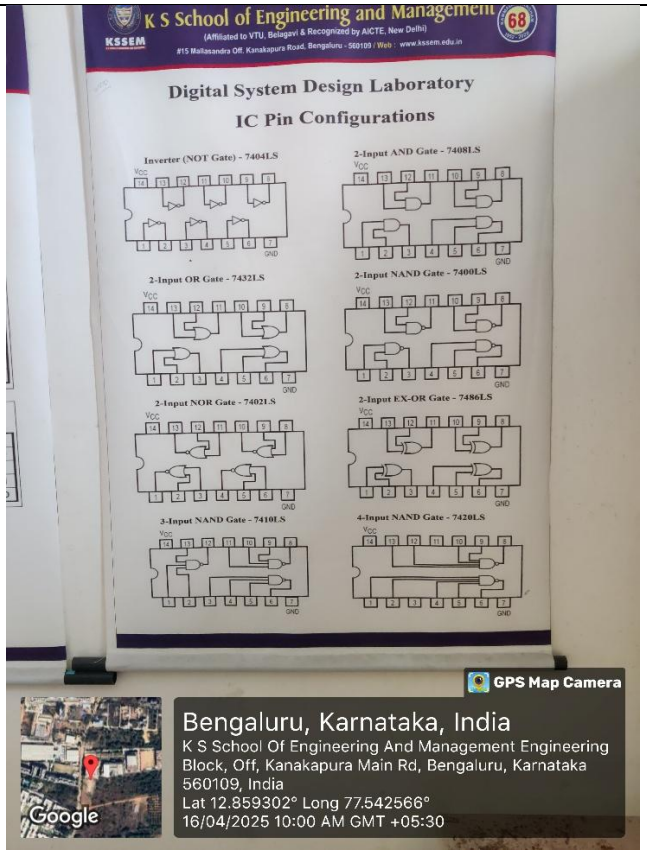
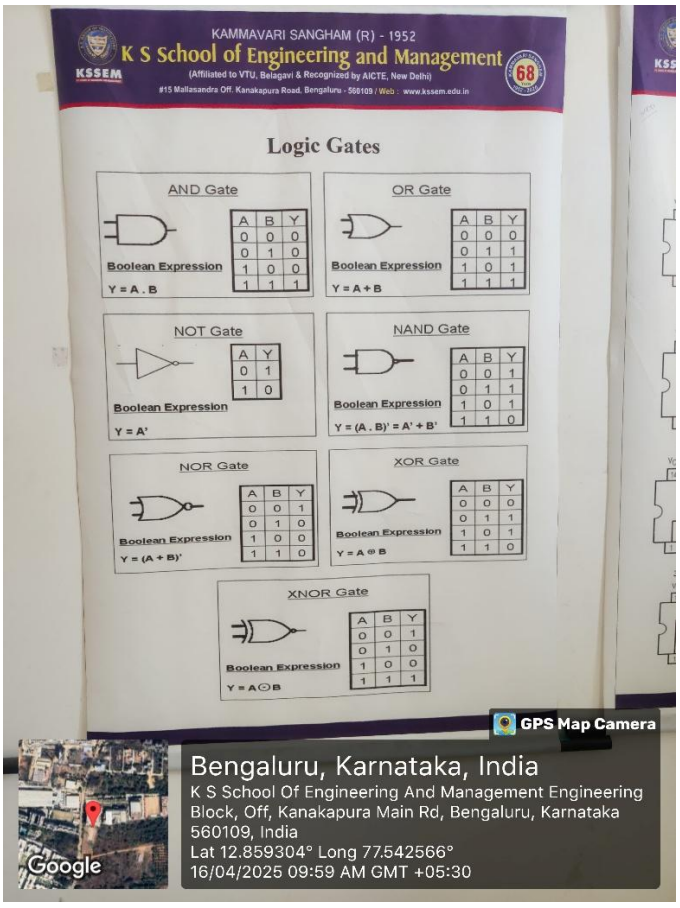
WORKING MODELS/CHARTS/MONOGRAMS, ETC

In order to ensure that the laboratory instructions are delivered more effectively, and to help the students use the equipment and technologies more easily, the department faculty members develop reference charts, in collaboration with professional designers. These charts are then printed and displayed in the laboratories, and help the students learn and effectively utilize the laboratory, during their curricular learning as well as their research and project work.

The following are some examples of Charts designed by the faculty, which are on display in the laboratories

Sl No	Laboratory Name	Charts with Description
1	Digital Signal Processing Laboratory	 Fig 5.7.3.1: Typical DSP System, Evolution of DSP Processors

		 Fig 5.7.3.2: 8051 Microcontroller
2	Embedded Controller Laboratory	 Fig 5.7.3.3: STM32F103RB Microcontroller

3	Electronic Devices and Instrumentation Laboratory	 Fig 5.7.3.4: Commonly used Boolean Gate ICs  Fig 5.7.3.5: Basic Logic Gates for Digital Design

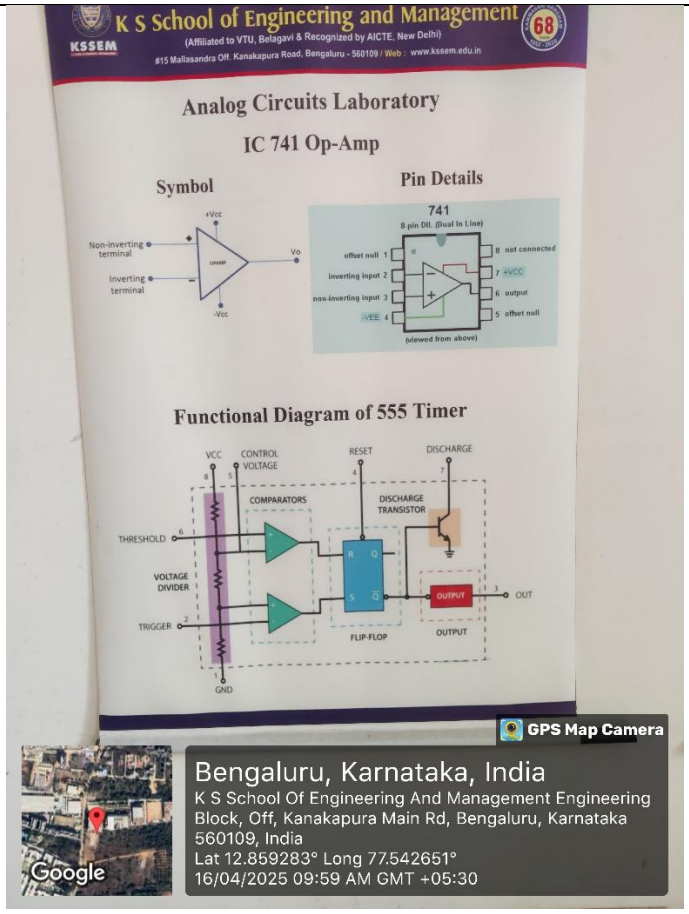


Fig 5.7.3.6: OpAmp IC 741, 55 Timer

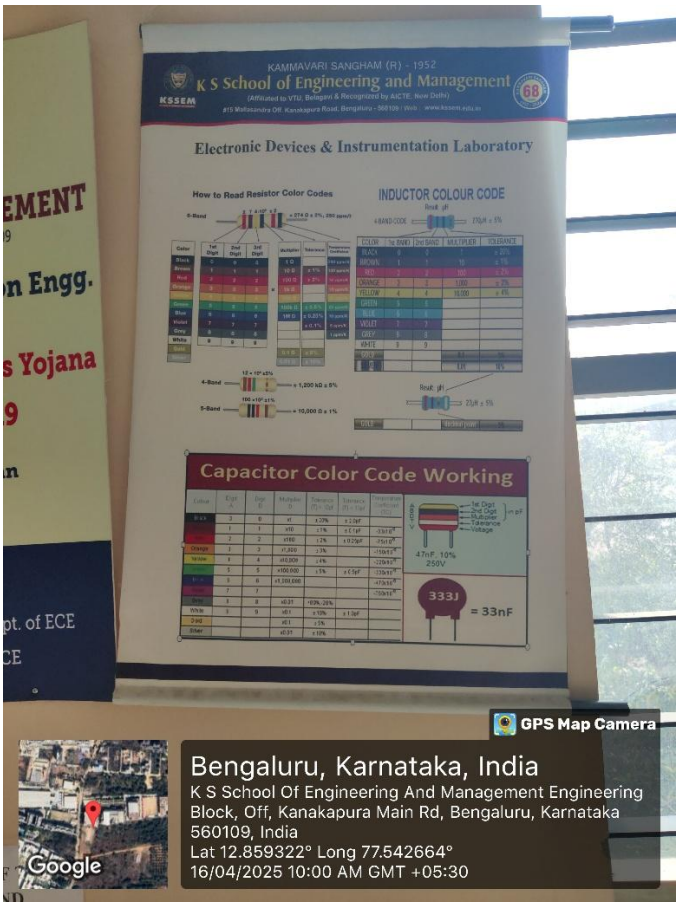
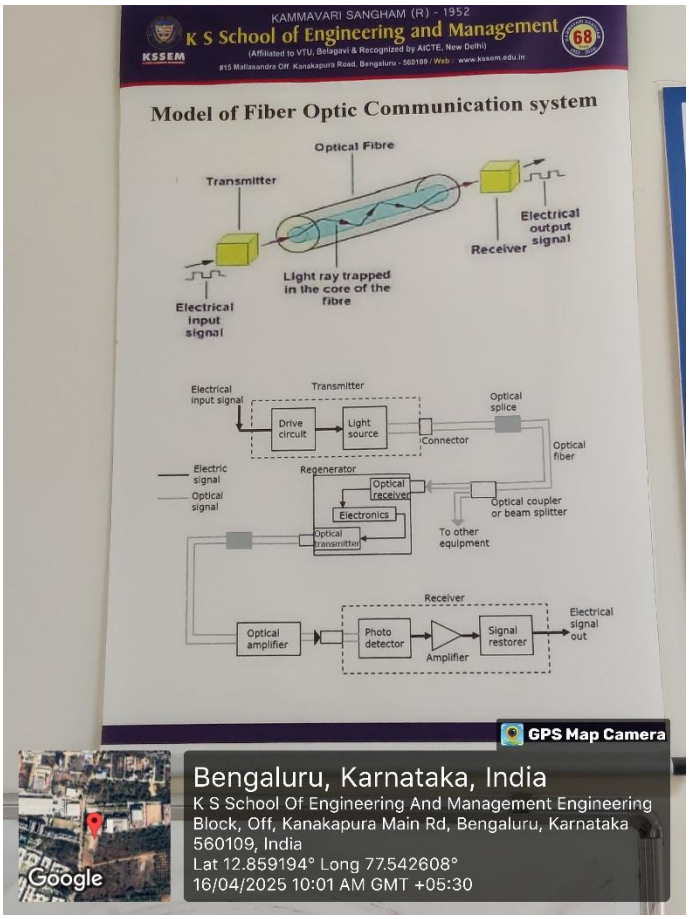
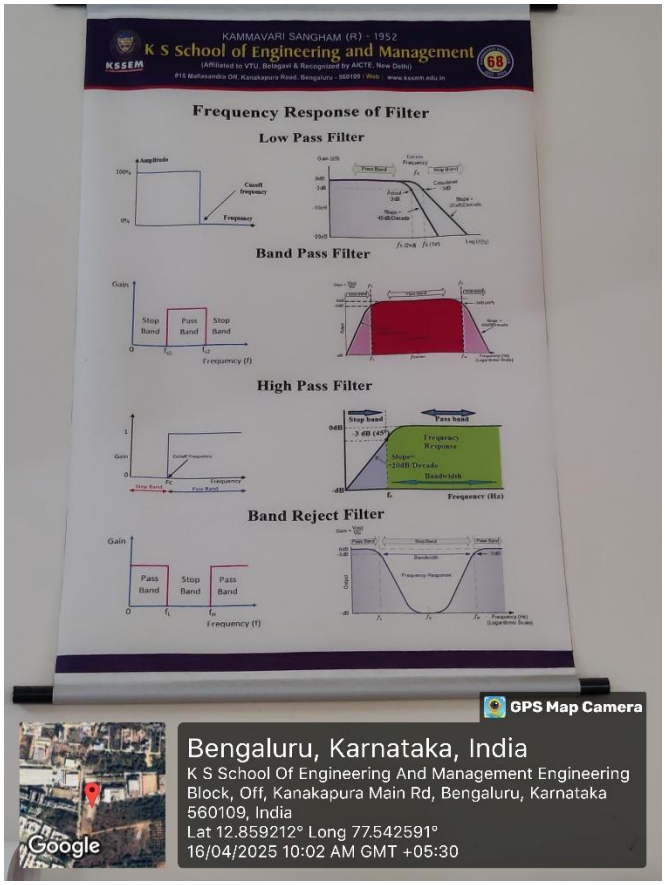
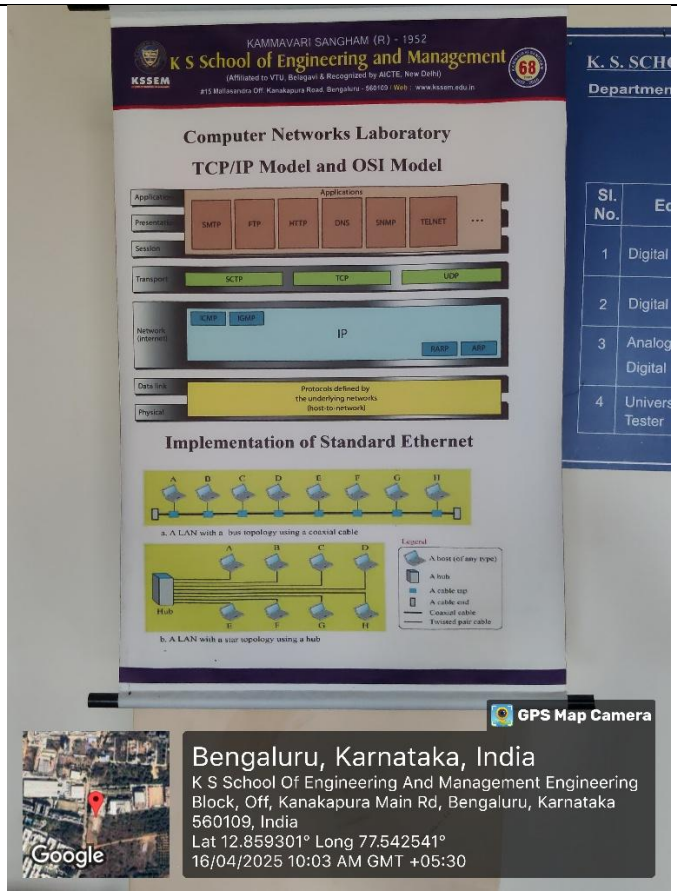
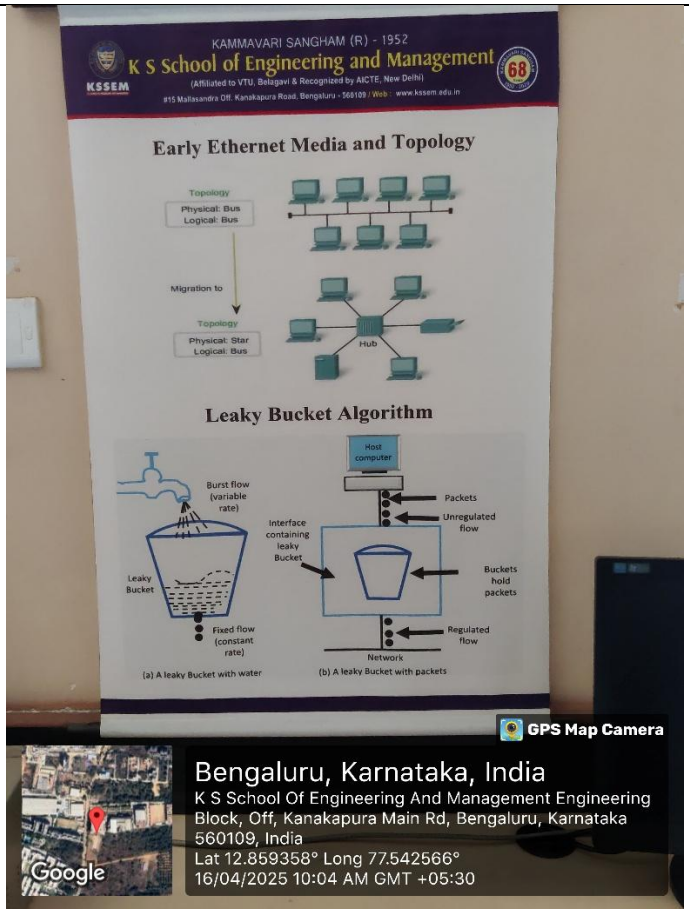
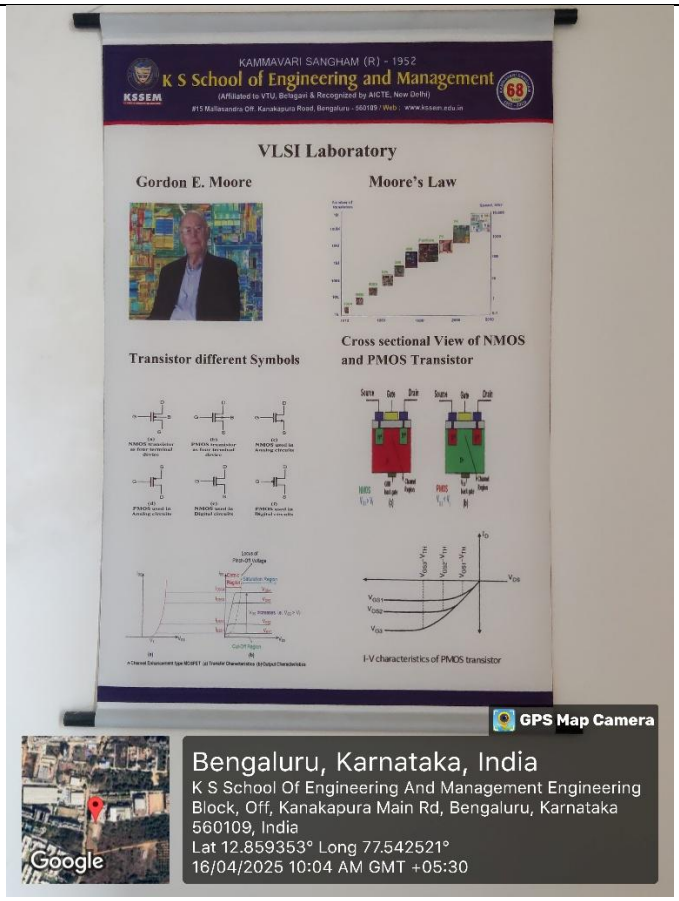
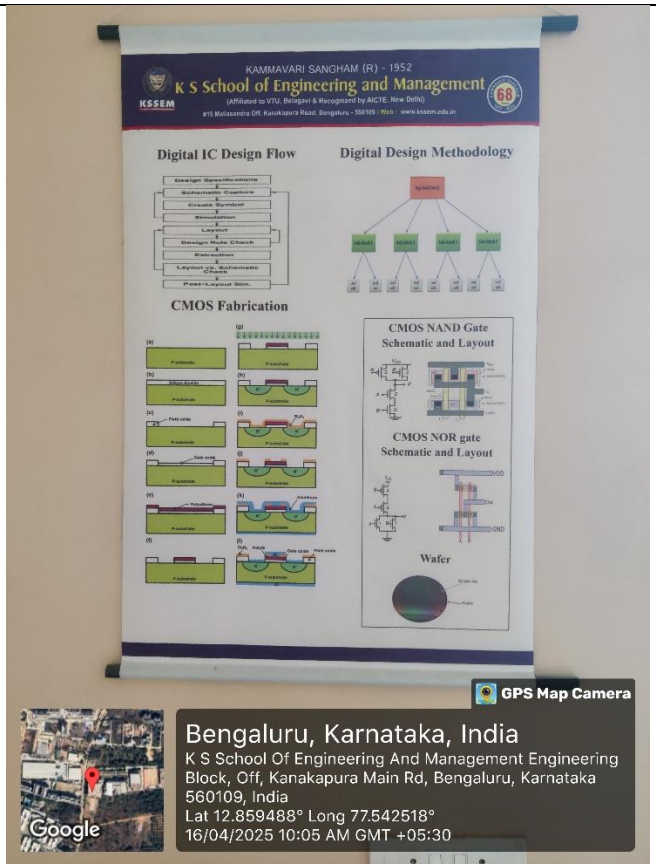
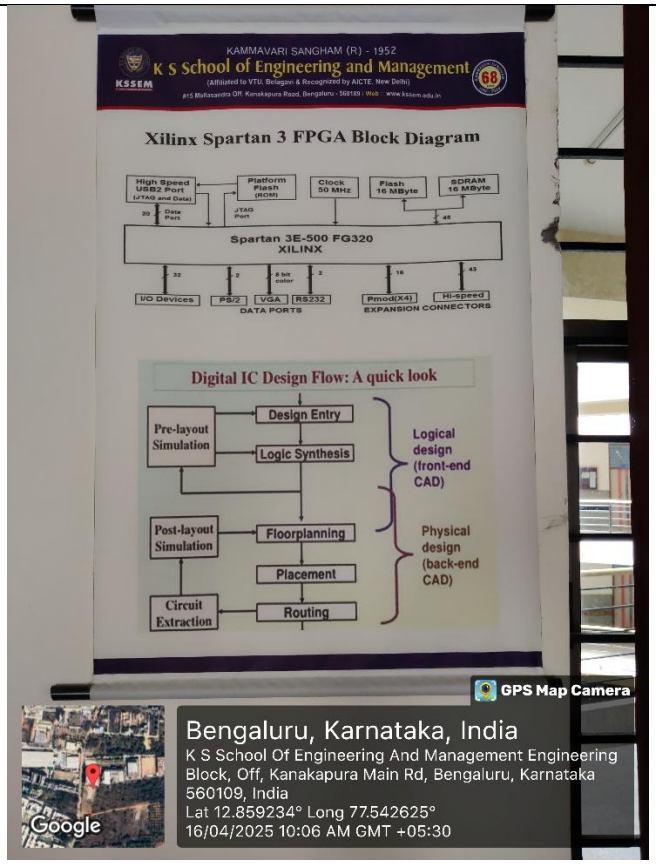


Fig 5.7.3.7: Resistance and Capacitance Colour Codes

4	Advanced Communication Laboratory	 Fig 5.7.3.8: Moculation Techniques  Fig 5.7.3.9: Fiber Optic Communication System

		 Fig 5.7.3.10: Filter Frequency Responses
5	Computer Networks Laboratory	 Fig 5.7.3.11: TCP/IP Models, Ethernet

		 Fig 5.7.3.12: Network Topologies, Leaky Bucket Algorithm
6	VLSI Laboratory	 Fig 5.7.3.13: Moore's Law, Transistor Symbols, Structures

		 Fig 5.7.3.14: IC Design Methodology, CMOS Structures
7	HDL Laboratory	 Fig 5.7.3.15: IC Design Workflow, Xilix Spartan 3 FPGA