

K S School of Engineering and Management

Department of Electronics and Communication Engineering

Semester: VI

VLSI Laboratory (21ECL66) Log Book

Academic Year-2023-24

04/12/2023

Sem :-

Section :-
VLSI labs

Batch :- A1

Mangal

1/20

S.NO.	Name	USN	CPU NO	Date	Log in	Log out	Sign
1.	K.Tawanth	1KG20EC017		1:20	KS	4:30	
2.	Atharsh	1KG20EC005		1:20		4:30	
3.	K.Gowtham	1KG20EC014		1:20	km	4:30	
4.	Junaid	1KG20EC061		1:20	fi	4:30	
5.	A.Yuvasee	1KG20EC002		1:20	YU	3:30	
6.	Jyothiswari	1KG20EC012		1:20	jk	4:30	
7.	Chandana.R	1KG20EC007		1:20	clt	5:00	clt
8.	Charitra	1KG20EC006		1:20	dr	5:00	dr
9.	Koushik	1KG20EC017		1:20	ks	3:30	
10.	Nehraj	1KG20EC010		1:20		4:00	
	Kishore	1KG20EC016		1:20		5:30	
12.	D.Thanya Deepika	1KG20EC008		1:20	Thuya	4:00	Thuya
13.	A.Rohith	1KG20EC003		1:20	Aro	4:00	Aro
14.	A.Harishkhan	1KG20EC001		1:20	HJ	4:00	HJ
15.	G.Bhoothath	1KG20EC013		1:20	BL	4:00	BL
16.	Priya G.C	1KG20EC011		1:20	px	4:00	px
	Akhathaa.s	1KG20EC004		1:20			

Analog: exp 2 = Nand Simulation

Muskaan

4/12/23

Dileep-T

Dileep
4/12/23

Professor & HOD

Dept. of Electronics & Communication Engineering
LS School of Engineering & Management
Santacruz - 366109

1-2022

Semi-JTH Section
VST Cells

Batch

papergrid

Sl. No.	Name	VSN	CPU No.	Leg	Sign Date: 09 Oct 2022	Sign Date: 09 Oct 2022
1	G. Bhadra	1KG20EC013	1:20	Blu	3:50 AM	AM
2	A. Hosgolla	1KG20EC01	1:20	Wht	3:50 AM	AM
3	Dhangya Deepika	1KG20 EC008	1:20	Orange	3:50 AM	AM
4	Gupta P	1KG20EC042	1:20	Sp	3:50 AM	AM
5	Nearaj	1KG20EC010	1:20	Sp	3:50 AM	AM
6	Yashaswini R	1KG20EC048	1:20	Dark	3:50 AM	AM
7	Uma. c	1KG20EC046	1:20	Red	3:50 AM	AM
8	Raghavendra..c	1KG20EC035	1:20	Red	3:50 AM	AM
9	Sadhana	1KG20EC038	1:20	Sadha	3:50 AM	AM
10	Pooja	1KG20EC011	1:20	Sp	3:50 AM	AM
11	Ayuvalli	1KG20EC002	1:20	Wht	3:50 AM	AM
12	Chandana.R	1KG20EC002	1:20	White	3:50 AM	AM
13	Chaitra.L	1KG20EC006	1:20	Chaitra	3:50 AM	AM
14	Sachin.R	1KG20EC037	1:20	Sp	3:50 AM	AM
15	S. Ushachowdary	1KG20EC040	1:20	Usha	3:50 AM	AM
16	S. Umachowdary	1KG20EC039	1:20	Uma	3:50 AM	AM
17	Shreetha.S	1KG20EC004	1:20	Shreetha	3:50 AM	AM
18	Rakshitha.N	1KG20EC036	1:20	Rakshitha	3:50 AM	AM
19	UJWALA.A	1KG20EC045	1:30	Ujwala	3:50 AM	AM
20	Debjhoree.M	1KG20EC049	1:20	Debjhoree	3:50 AM	AM
21	Tanuja.N	1KG20EC048	1:20	Tanuja	3:50 AM	AM
22	G. Gopikumarreddy	1KG20EC012	1:20	Gopi	3:50 AM	AM
23	Koushik	1KG20EC017	1:20	Koushik	3:50 AM	AM
24	Rohith	1KG20EC003	1:20	Rohith	3:50 AM	AM
25	V. Sandhar	1KG20EC047	1:20	V. Sandhar	3:50 AM	AM
26	Umesh	1KG20EC016	1:20	Umesh	3:50 AM	AM

Exh 2d, e, f: SRff, JKlatch, JKff

Muskaan

04/11/22

Rishika V.T

Kishan

Semester - 7th Section - A Batch - A3

papergrid

NO.	Name	CSN	CPU NO.	log on	8 AM	Rate: 160/- out 8 AM
1.	S. Usha chowdary	1KG20EC040		1:20	user	3:50 user
2.	S. Uma chowdary	1KG20EC039		1:20	user	3:50 user
3.	Boghaundra e	1KG20EC035		1:20	Re	3:50 Re
4.	Swetha, m	MEG21B School		1:20	Re	3:50 Re
5.	Kleishanth, m	1KG21EC004		1:20	AB	3:50 AB
6.	Uma.c	1KG20EC046		1:20	user	3:50 user
7.	Yashaswini.R	1KG20EC048		1:20	user	3:50 user
8.	Tejaswree.M	1KG20EC044		1:20	TJ	3:50 TJ
9.	Sathanaa HD	1KG20EC038		1:20	Sudha	3:50 Sudha
10.	Suplith, p	1KG20EC042		1:20	SP	3:50 SP
11.	V.Sandhar	1KG20EC047		1:20	Re	3:50 Re
12.	Rakeshika M	1KG20EC036		1:20	Re	3:50 Re

A.U & Address Completed

Kenika.V - T Radhika
10/11/23

Dileep.J
(coll)

✓ P.J

Professor & Head

Dept. of Electronics & Communication Engineering
K.S. School of Engineering & Management
Mysore - 560 102

23

Sem :- 7th Semester

Batch :-

papergrid

No.	Name	OSN	CPU NO.	Log In	Date:	Log Out	Time
1.	Nithin G.N	1KG20EC029		1:00	2	3:40	10
2	M.B.Hemanth	1KG20EC019		1:00	M.B.H	3:40	MBH
3	M.Jandana	1KG20EC023		1:00	Jandana	3:50	Janda
4	N.Simi	1KG20EC026		1:00	Simi	3:50	Simi
5	T.Haritha	1KG20EC030		1:00	Haritha	3:50	Haritha
6	M.Kainkalya	1KG20EC022		1:00	Kainkalya	3:50	Kain
7	Banuprasad	1KG20EC024		1:00	banu	3:40	banu
8	Neha Kurnali S	1KG20EC027		1:00	Neha	3:50	Neha
9	Nishanthan	1KG20EC028		1:00	Nishanthan	3:50	N.
10	V.Bhaskar	1KG20EC025		1:00	V.Bhaskar	3:40	Bhaskar
11	M.Hemanth	1KG20EC021		1:00	A	3:50	A
12	K.Raviteja	1KG20EC018		1:00	K.R	3:50	K.R
13	Punith.P	1KG20EC033		1:00	P	3:50	P.
14.	Pooja.R	1KG20EC031		1:00	Pooja	3:50	Poo.
15.	Psaiikethan	1KG20EC032		1:20	psai	3:50	psai
16	M.Loraa	1KG20EC020		1:20	Loraa	3:40	Loraa
	R.Parthiban	1KG20EC034		1:20	R.P	3:50	R.P

Analog part Inverter upto simulation of
schematic

Lenuka. V.T

Final
15/11/23

Dileep T
15/11/23



Professor & HOD

Dept. of Electronics & Communication Engineering
K.S. School of Engineering & Management
Songapore - 560 105

Semester Test Part - I

11/23

papergrid

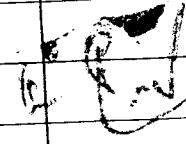
L.NO.	Name	USN	GP No.	Leg	8pm	Date	Lab	18pm
1.	K. Jayawanth	1KG120EC015		1:20	KPR	3:45	1st	
2.	K. Gowtham	1KG120EC014		1:20	Var	3:45	2nd	
3.	G. Pooja	1KG120EC011		1:20	Par	3:30	3rd	
4.	A. Rohith	1KG120EC003		1:20	Afsh	3:30	4th	
5.	A. Yuvaree	1KG120EC002		1:20	Yuvaree			
6.	Chandana R.	1KG120EC007		1:20	Chdr	3:30	Chdr	
7.	Chaitra L	1KG120EC006		1:20	Chit	3:30	Chit	
8.	Hariharth	1KG120EC001		1:20	Der	3:30	Der	
9.	Bharath	1KG120EC013		1:20	Ber	3:30	Ber	
10.	Akshamthas	1KG120EC004		1:30	Muktha			
11.	G. Neeraj	1KG120EC010		1:30	De	3:30	De	
12.	Raj. Janard	1KG119EP061		1:20	Re	3:20	Re	

Analog part:
Structure of Semester - Layout

Muktha
27/11/23

Renuka V - Final
27/11/23

Dileep J
27/11/23



Dept. of Electrical & Electronics Engineering
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Bengaluru - 560 109