

SCHEME OF TEACHING AND EXAMINATION
B.E Electronics & Communication Engineering / Telecommunication Engineering
(Common to Electronics & Communication and Telecommunication Engineering)

III SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17MAT31	Engineering Mathematics –III*	Maths	04		03	60	40	100	4
2	17EC32	Electronic Instrumentation	EC	03		03	60	40	100	3
3	17EC33	Analog Electronics	EC	04		03	60	40	100	4
4	17EC34	Digital Electronics	EC	04		03	60	40	100	4
5	17EC35	Network Analysis	EC	04		03	60	40	100	4
6	17EC36	Engineering Electromagnetics	EC	04		03	60	40	100	4
7	17ECL37	Analog Electronics Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECL38	Digital Electronics Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
9	17KL/CPH39/49	Kannada/Constitution of India, Professional Ethics and Human Rights	Humanities	01		01	30	20	50	01
TOTAL				Theory: 24hours Practical: 06 hours		25	510	340	850	28

1.Kannada/Constitution of India, Professional Ethics and Human Rights: 50 % of the programs of the Institution have to teach Kannada/Constitution of India, Professional Ethics and Human Rights in cycle based concept during III and IV semesters.

2. Audit Course:

(i) *All lateral entry students (except B.Sc candidates) have to register for Additional Mathematics – I, which is 03 contact hours per week.

1	17MATDIP31	Additional Mathematics –I	Maths	03		03	60	--	60	--
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(ii) Language English (Audit Course) be compulsorily studied by all lateral entry students (except B.Sc candidates)

**B.E Electronics & Communication Engineering / Telecommunication Engineering
(Common to Electronics & Communication and Telecommunication Engineering)**

IV SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17MAT41	Engineering Mathematics –IV*	Maths	04		03	60	40	100	4
2	17EC42	Signals and Systems	EC	04		03	60	40	100	4
3	17EC43	Control Systems	EC	04		03	60	40	100	4
4	17EC44	Principles of Communication Systems	EC	04		03	60	40	100	4
5	17EC45	Linear Integrated Circuits	EC	04		03	60	40	100	4
6	17EC46	Microprocessor	EC	03		03	60	40	100	3
7	17ECL47	Microprocessor Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECL48	Linear ICs and Communication Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
9	17KL/CPH39/49	Kannada/Constitution of India, Professional Ethics and Human Rights	Humanities	01		01	30	20	50	01
TOTAL				Theory: 24hours Practical: 06 hours		25	510	340	850	28

1. Kannada/Constitution of India, Professional Ethics and Human Rights: 50 % of the programs of the Institution have to teach Kannada/Constitution of India, Professional Ethics and Human Rights in cycle based concept during III and IV semesters.

2. Audit Course:

(i) *All lateral entry students (except B.Sc candidates) have to register for Additional Mathematics – II, which is 03 contact hours per week.

1	17MATDIP41	Additional Mathematics –II	Maths	03		03	60	--	60	--
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(ii) Language English (Audit Course) be compulsorily studied by all lateral entry students (except B.Sc candidates)

B.E.: Electronics & Communication Engineering

V SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17ES51	Management and Entrepreneurship Development	EC	04		03	60	40	100	4
2	17EC52	Digital Signal Processing	EC	04		03	60	40	100	4
3	17EC53	Verilog HDL	EC	04		03	60	40	100	4
4	17EC54	Information Theory & Coding	EC	04		03	60	40	100	4
5	17EC55X	Professional Elective-1	EC	03		03	60	40	100	3
6	17EC56X	Open Elective-1	EC	03		03	60	40	100	3
7	17ECL57	DSP Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECL58	HDL Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
TOTAL				Theory: 22hours Practical: 06 hours		24	480	320	800	26

Professional Elective-1		Open Elective – 1*** (List offered by EC/TC Board only)	
17EC551	Nanoelectronics	17EC561	Automotive Electronics
17EC552	Switching & Finite Automata Theory	17EC562	Object Oriented Programming Using C++
17EC553	Operating System	17EC563	8051 Microcontroller
17EC554	Electrical Engineering Materials		
17EC555	MSP430 Microcontroller		

***Students can select any one of the open electives offered by any Department (Please refer to consolidated list of VTU for open electives).

Selection of an open elective is not allowed, if:

- The candidate has no pre – requisite knowledge.
- The candidate has studied similar content course during previous semesters.
- The syllabus content of the selected open elective is similar to that of Departmental core course(s) or to be studied Professional elective(s).

Registration to open electives shall be documented under the guidance of Programme Coordinator and Adviser.

B.E.: Electronics & Communication Engineering

VI SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17EC61	Digital Communication	EC	04		03	60	40	100	4
2	17EC62	ARM Microcontroller & Embedded Systems	EC	04		03	60	40	100	4
3	17EC63	VLSI Design	EC	04		03	60	40	100	4
4	17EC64	Computer Communication Networks	EC	04		03	60	40	100	4
5	17EC65X	Professional Elective-2	EC	03		03	60	40	100	3
6	17EC66X	Open Elective-2	EC	03		03	60	40	100	3
7	17ECL67	Embedded Controller Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECL68	Computer Networks Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
TOTAL				Theory: 22hours Practical: 06 hours		24	480	320	800	26

Professional Elective-2		Open Elective – 2*** (List offered by EC/TC Board only)	
17EC651	Cellular Mobile Communication	17EC661	Data Structures Using C++
17EC652	Adaptive Signal Processing	17EC662	Power Electronics (<i>not for E&C students</i>)
17EC653	Artificial Neural Networks	17EC663	Digital System Design using Verilog
17EC654	Digital Switching Systems		
17EC655	Microelectronics		

***Students can select any one of the open electives offered by any Department (Please refer to consolidated list of VTU for open electives).

Selection of an open elective is not allowed, if:

- The candidate has no pre – requisite knowledge.
- The candidate has studied similar content course during previous semesters.
- The syllabus content of the selected open elective is similar to that of Departmental core course(s) or to be studied Professional elective(s).

Registration to open electives shall be documented under the guidance of Programme Coordinator and Adviser.