K. S. School of Engineering and Management Department of Electronics and Communication Engineering

First Internal Test Time Table Aug-Nov 2019

	2 04	1 03	No	SI.	Semester-III
05/09/2019	/09/2019	03/09/2019	Çarç	Date	er- III
Thursday	04/09/2019 Wednesday	Tuesday	ا ا	Dav	=
Computer Organization & Architecture	Electronic Devices (18EC33)	Transform Calculus, Fourier Series And Numerical Techniques (18MAT31)	Subject (Code)	Morning Session Time: 9.30 am To 11 am	
Instrumentation (18EC36)	(18EC34)	(18EC32) Digital System Design	Subject (Code)	After Noon Session Time: 1.30 pm To 3.00 pm	

Signature of the rest Coordinator

Signature of the MOD

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Date: 22/08/2019

K. S. School of Engineering and Management Department of Electronics and Communication Engineering

First Internal Test Time Table Aug-Nov 2019

			Date: 44/00/4017
Ea		Morning Session	After Noon Session
Date	Day	Time: 9.30 am To 11 am	Time: 1.30 pm To 3.00 pm
		Subject (Code)	Subject (Code)
		Management and Entrepreneurship	Digital Signal Processing
03/09/2019	Tuesday	Development	(17EC52)
		(17EC51)	3.0
04/09/2019	Wednesday	Verilog HDL	Information Theory and Coding
	, cancomy	(17EC53)	(17EC54)
		Operating System	Automotive Electronics / C++
05/09/2019	Ihursday	(17EC553)	(17EC561/17EC562)
	Date 03/09/2019 04/09/2019 05/09/2019	Th We	

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K. S. School of Engineering and Management Department of Electronics and Communication Engineering

First Internal Test Time Table Aug-Nov 2019

The state of the s	COLLICOCOL Y EX			Date: HH COLLON
3	d a		Morning Session	After Noon Session
2 5	Date	Day	Time: 9.30 am To 11 am	Time: 1.30 pm To 3.00 pm
NO		-	Subject (Code)	Subject (Code)
_	03/00/2010	Tungday	Microwave & Antenna	Digital Image Processing
-	03/03/2013	I ucsuay	(15EC71)	(15EC72)
	ř.		Power Electronics	Real Time Systems / Multimedia
2	04/09/2019	04/09/2019 Wednesday	(15EC73)	Communication
			300	(15EC743/15EC741)
٠	05/00/2010		Satellite Communication	
S	05/09/2019	Inursday	(15EC755)	

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Department of Electronics and Communication Engineering K. S. School of Engineering and Management

First Internal Test Time Table Aug-Nov 2019

(15 Scheme)

Sen	Semester- V			Date: 22/08/2019
3			Morning Session	After Noon Session
27.	Date	Day	Time: 9.30 am To 11 am	Time: 1.30 pm To 3.00 pm
NO			Subject (Code)	Subject (Code)
		, S	Management and	Digital Signal Processing
—	03/09/2019	Tuesday	Entrepreneurship Development (15EC51)	(15EC52)
٠	0.4/00/2010	VIII - January	Verilog HDL	Information Theory and Coding
1	2 04/09/2019 Wednesday	wednesday	(15EC53)	(15EC54)
			Operating System	Automotive Electronics / C++
ယ	05/09/2019	Thursday	(15EC553)	(15EC561/15EC562)
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1-120 5 principal 26/8/18

Semester- III

First Internal Test Time Table Aug-Nov 2019

Date: 28/8/2019

Department of Electronics and Communication Engineering

K. S. School of Engineering and Management

	-			
	6/8/2019 Friday		Date	
i i	Friday		Dav	
Law (CPC) 18CPC39/49	Constitution of India, Professional Ethics and Cyber	Subject (Code)	Time:8.40 to 9.35	Morning Session

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Signature of Principal 18/15